

Amendments to the Claims:

The following listing of claims will replace all prior versions, and listings, of claims in the application:

1. (Previously Presented) An electronic circuit, comprising:
a first transistor including a first terminal, a second terminal, and a first control terminal;
a second transistor including a third terminal, a fourth terminal, and a second control terminal, the third terminal being coupled to the first control terminal;
a capacitive element including a first electrode and a second electrode, the first electrode being coupled to the first control terminal; and
a third transistor including a fifth terminal and a sixth terminal, the fifth terminal being coupled to the second electrode, the second control terminal being coupled to the third terminal.
2. (Previously Presented) The electronic circuit according to Claim 1, further comprising a fourth transistor including a seventh terminal and an eighth terminal, the seventh terminal being coupled to the fourth terminal.
3. (Previously Presented) The electronic circuit according to Claim 1, the first terminal being coupled to an electronic element.
4. (Previously Presented) The electronic circuit according to Claim 3, the electronic element being a current-driven element.
5. (Previously Presented) An electronic circuit, comprising:
a plurality of first signal lines;
a plurality of power lines; and
a plurality of unit circuits, each of the plurality of unit circuits further comprising:

a first transistor including a first terminal, a second terminal, and a first control terminal;

a second transistor including a third terminal, a fourth terminal, and a second control terminal, the third terminal being coupled to the first control terminal;

a capacitive element including a first electrode and a second electrode, the first electrode being coupled to the first control terminal; and

a third transistor including a fifth terminal, a sixth terminal, and a third control terminal, the fifth terminal being coupled to the second electrode,

the second control terminal being coupled to the third terminal, and

the third control terminal being coupled to a respective first signal line of the plurality of first signal lines.

6. (Currently Amended) The electronic circuit according to Claim 5, further comprising a plurality of second signal lines, each of the plurality of unit circuits further ~~comprising~~ including a fourth transistor including a seventh terminal, an eighth terminal, and a fourth control terminal, the seventh terminal being coupled to the fourth terminal, and the fourth control terminal being coupled to a respective second signal line of the plurality of second signal lines.

7. (Previously Presented) The electronic circuit according to Claim 5, the first terminal being coupled to an electronic element.

8. (Previously Presented) The electronic circuit according to Claim 7, the electronic element being a current-driven element.

9. (Previously Presented) An electronic circuit, comprising:
a holding element that holds a signal as a charge;
a first switching transistor that controls the transfer of the signal to the holding element;

a driving transistor of which a conduction state is set according to the charge held by the holding element; and

an adjustment transistor that sets a control terminal of the driving transistor to a predetermined potential prior to the transfer of the signal to the holding element.

10. (Previously Presented) The electronic circuit according to Claim 9, further comprising a second switching transistor that controls the electronic connection or the electronic disconnection between the adjustment transistor and the predetermined potential.

11. (Previously Presented) The electronic circuit according to Claim 9, the driving transistor being coupled to an electronic element.

12. (Previously Presented) The electronic circuit according to Claim 11, the electronic element being a current-driven element.

13. (Previously Presented) A driving method for an electronic circuit comprising: a first transistor including a first terminal, a second terminal, and a first control terminal;

a second transistor including a third terminal and a fourth terminal, the third terminal being coupled to the first control terminal; and

a capacitive element including a first electrode and a second electrode, the first electrode being coupled to the first control terminal,

the driving method comprising:

electronically connecting the fourth terminal to a predetermined potential for setting the first control terminal to a first potential; and

when the fourth terminal is electronically disconnected from the predetermined potential, changing the potential of the second electrode of the capacitive element from a second potential to a third potential to change the potential of the first control terminal from the first potential.

14. (Previously Presented) The driving method for the electronic circuit according to Claim 13,

the potential of the second electrode being set to the second potential while at least the fourth terminal is electronically connected to a predetermined potential is being performed.

15. (Currently Amended) An electro-optical device, comprising:

- a plurality of scanning lines;
- a plurality of data lines;
- a plurality of power lines; and
- a plurality of unit circuits having electro-optical elements,
each of the plurality of unit circuits ~~further~~ comprises:
 - a first transistor including a first terminal, a second terminal, and a first control terminal;
 - an electro-optical element coupled to the first terminal;
 - a second transistor including a third terminal, a fourth terminal, and a second control terminal, the third terminal being coupled to the first control terminal;
 - a capacitive element including a first electrode and a second electrode, the first electrode being coupled to the first control terminal;
 - a third transistor including a fifth terminal, a sixth terminal, and a third control terminal, the fifth terminal being coupled to the second electrode; and
 - a fourth transistor including a seventh terminal and an eighth terminal, the seventh terminal being coupled to the fourth terminal,

the second control terminal is coupled to the third terminal,
the third control terminal is coupled to one of the plurality of scanning lines,
and
the sixth terminal is coupled to one of the plurality of data lines.

16. (Previously Presented) The electro-optical device according to Claim 15, the electro-optical element being an organic EL element.

17. (Currently Amended) An electro-optical device comprising a plurality of scanning lines, a plurality of data lines, a plurality of power lines, and a plurality of unit circuits ~~having electro-optical elements~~, each of the plurality of unit circuits ~~comprises~~ including:

an electro-optical element;

a first switching transistor of which ~~the~~ a conduction state is controlled according to a scanning signal ~~sent~~ supplied through one ~~corresponding~~ scanning line of the plurality of scanning lines;

a holding element that accumulates a data signal ~~sent~~ supplied through one data line of the plurality of data lines and the first switching transistor, as a charge;

a driving transistor of which ~~the continuity~~ a conduction state is set according to ~~the~~ an amount of the charge accumulated by the holding element, ~~that~~ and supplies ~~current~~ having a current level according to ~~a~~ the conduction state to the electro-optical elements; and

an adjustment transistor that sets a control terminal of the driving transistor to a predetermined potential prior to the ~~transfer~~ supply of the data signal to the holding element.

18. (Previously Presented) The electro-optical device according to Claim 17, each of the plurality of unit circuits comprises a second switching transistor that controls the electronic connection or the electronic disconnection between the adjustment transistor and the predetermined potential.

19. (Previously Presented) The electro-optical device according to Claim 17, the electro-optical elements being organic EL elements.

20. (Currently Amended) A driving method for an electro-optical device ~~comprising including~~ a plurality of scanning lines, a plurality of data lines, a plurality of power lines, and a plurality of unit circuits ~~including, each of which includes:~~

a first transistor having a first terminal, a second terminal, and a first control terminal;

a second transistor having a third terminal and a fourth terminal, the third terminal being coupled to the first control terminal; and

_____ a capacitive element including a first electrode and a second electrode, the first electrode being coupled to the first control terminal,

the driving method comprising:

electronically ~~coupling~~ connecting the fourth terminal to a predetermined potential and of setting the first control terminal to a first potential; ~~and~~

after a scanning signal is sent through one scanning line of the plurality of scanning lines to a third transistor of which one end is coupled to the second electrode to turn on the third transistor, and

_____ when the fourth terminal is electronically disconnected from the predetermined potential, applying the voltage corresponding to a data signal from one data line of the plurality of data lines to the second electrode through the third transistor to change the potential of the second electrode from a second potential to a third potential to change the potential of the first control terminal from the first potential.

21. (Previously Presented) The driving method for the electro-optical device according to Claim 20,

the potential of the second electrode of the capacitive element being set to the second potential while at least the coupling of the fourth terminal to a predetermined potential is being performed.

22. (Previously Presented) The electronic apparatus having mounted thereon an electronic circuit according to Claim 1.

23. (Previously Presented) The electronic apparatus having mounted thereon an electro-optical device according to Claim 15.